

Abstracts

A Smith Chart Represented by a Neural Network and its Applications

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The Smith chart, while being a very useful graphical tool for the analysis and design of high frequency circuits, is subject to manual interpretation errors. A normalized impedance and admittance chart (Y-Z Smith chart) represented by a neural network type distributed computing system is developed for design automation. Two examples showing the use of such a neural network to design an impedance matching circuit are presented.

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